

Figure 3

Sensor Array overlaid with Bayer pattern

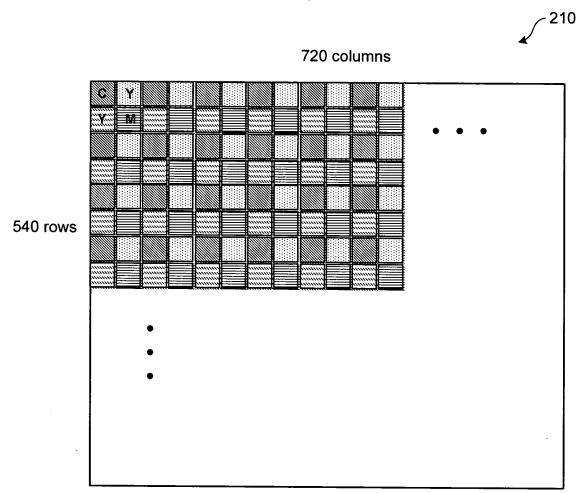


Figure 4

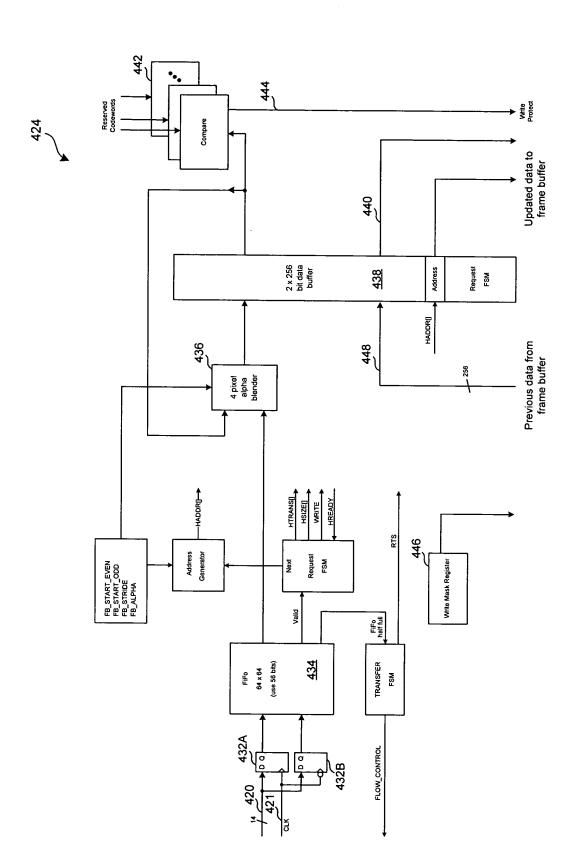
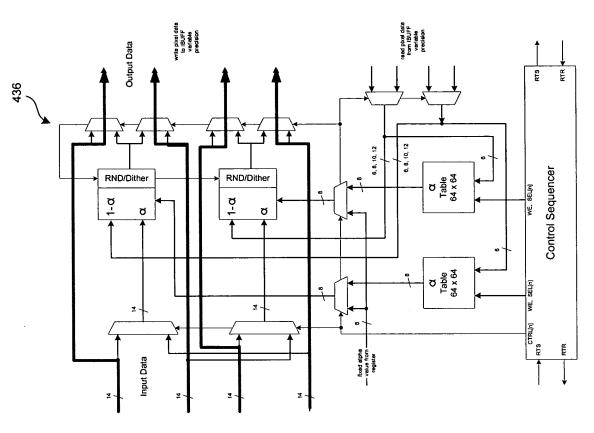
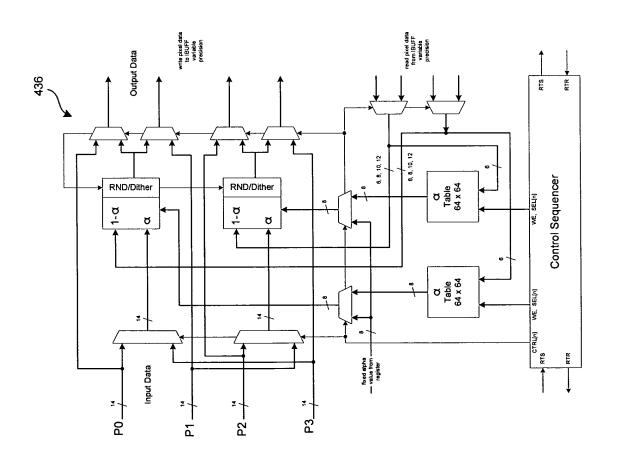


Figure 5

Title: Image Processor with Noise Reduction Circuit Inventor: Michael Frank Attorney Docket No: PIX-P-053

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write pixel data to IBUFF variable precision

RND/Dither

ಠ

4 .

Output Data

RND/Dither

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Input Data

460

436

α Table 64 x 64

C Table 64 x 64

462



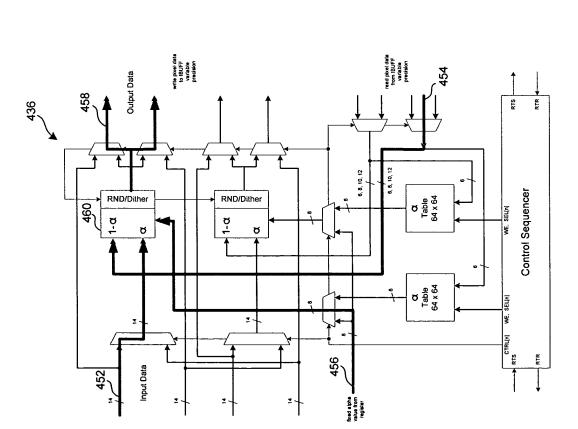
RTR

Control Sequencer

WE, SELIN

CTRL[n]

RTR



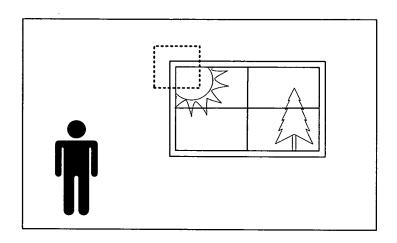
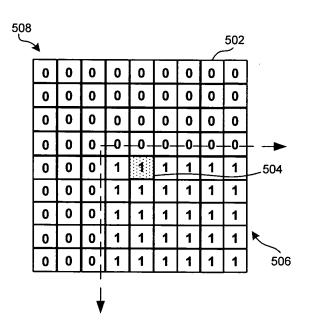


Figure 10



Blending Mask								510
.2	.2	.2	.2	.2	.2	.2	.2	.2
.2	1	1	1	1	1	1	1	.2
.2	1	2	2	2	2	2	1	.2
.2	1	2	4	4	4	2	1	.2
.2	1	2	4	8	4	2	1	.2
.2	1	2	4	4	4	2	1	.2
.2	1	2	2	2	2	2	1	.2
.2	1	1	1	1	1	1	1	.2
.2	.2	.2	.2	.2	.2	.2	.2	.2

Figure 13

Figure 14

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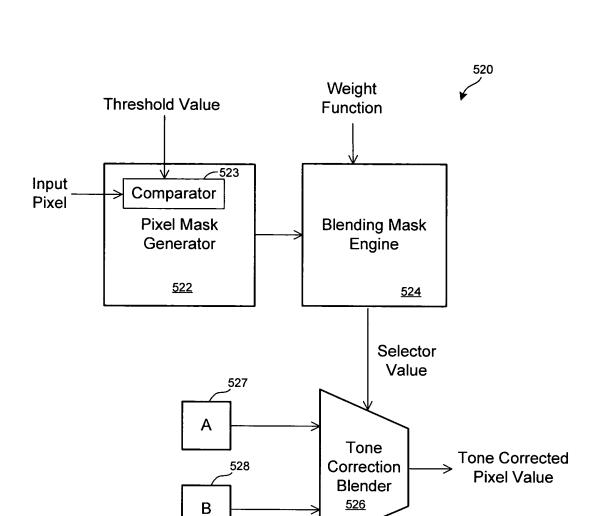


Figure 11

